

# SJSemi Company Profile

2020 3

P I L L A R S   O F   P E R F O R M A N C E

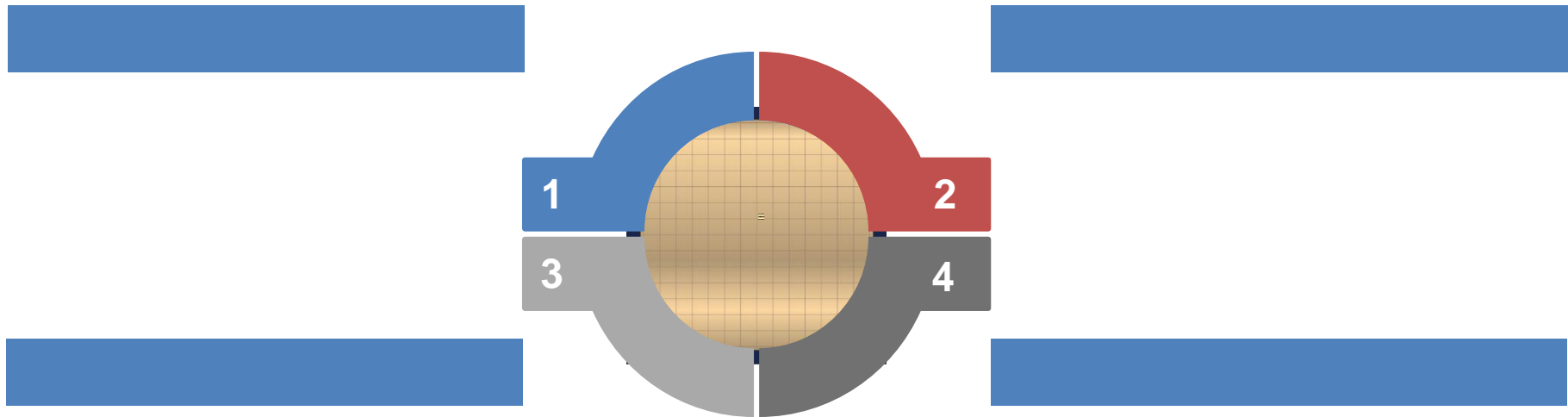




# 中芯长电半导体

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first Middle-End-Of-Line Entity that focuses on advanced bumping production



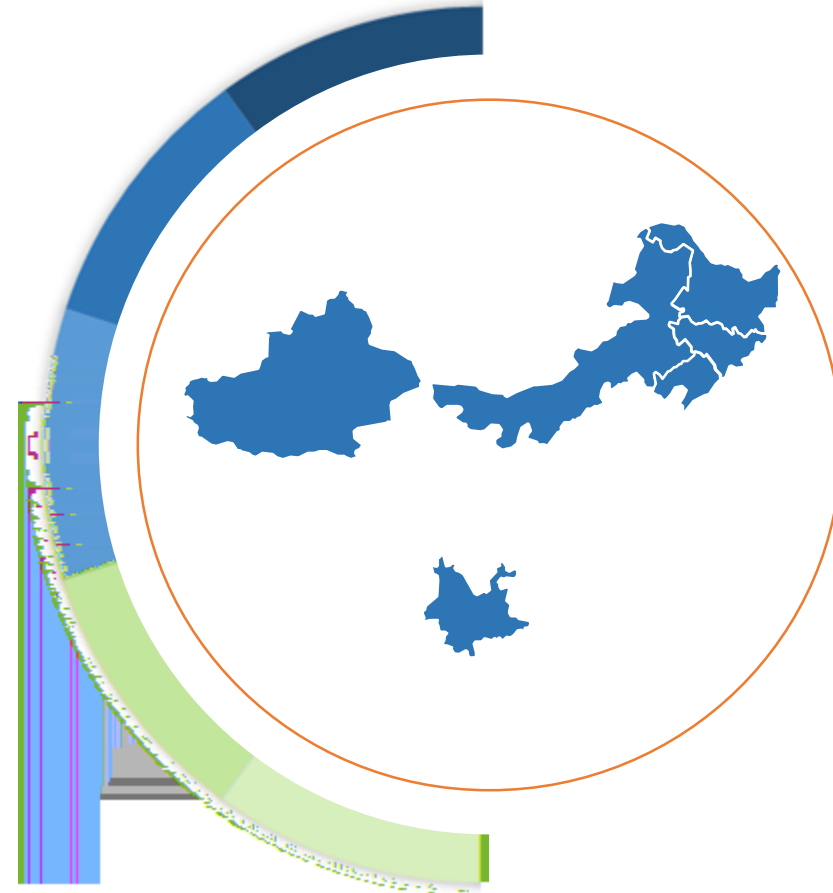
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Project Announcement



2<sup>nd</sup> round \$280M



5B RMB from CDB



Merge M1

Feb. 2014

Sep. 2014

Sep. 2015

Feb. 2016

Sept. 2017

Sep. 2018

Jan. 2019

Taking off

Phase I Production N28 / N14nm HVM

Phase I Production J2A Construction Complete



**ASIC**

- O n
- o n
- S O n
- O o

**SMIC & Other Foundries**

**D s o S**

- O o
- n D s
- S m
- D m s
- n n

**SJ-SH 200mm  
SJ-JY 300mm**

**S o**

- O
- y o
- y s
- s s o
- y n n

**SJ-SH & JY**

**Die Process**

- D o o
- D o o
- O o
- o
- s o n

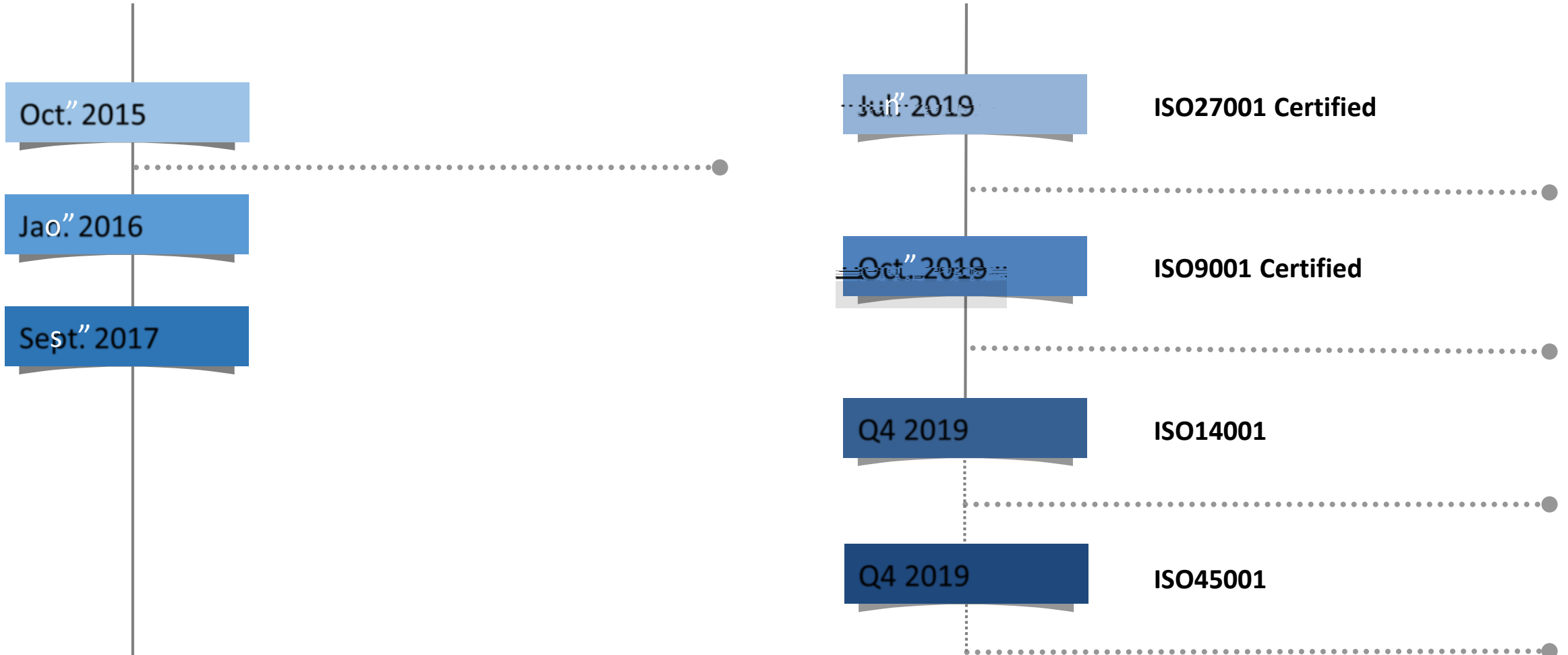
**SJ-SH & JY**

**Drop Ship**

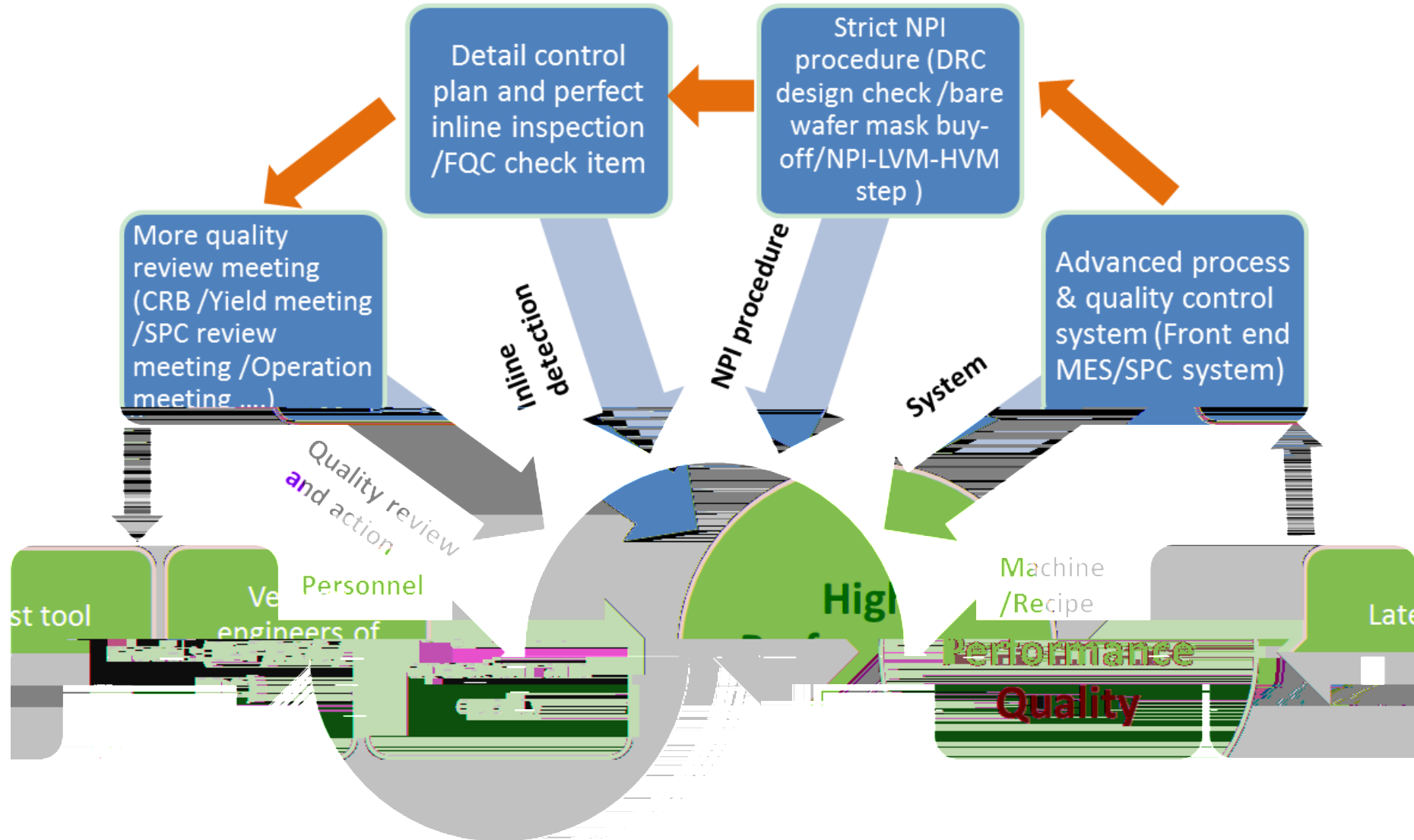
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**SJ-SH & JY**





# Six Key Points to Keep High Performance Quality





SEMICON  
CHINA

FPDCHINA

CSTIC 2019

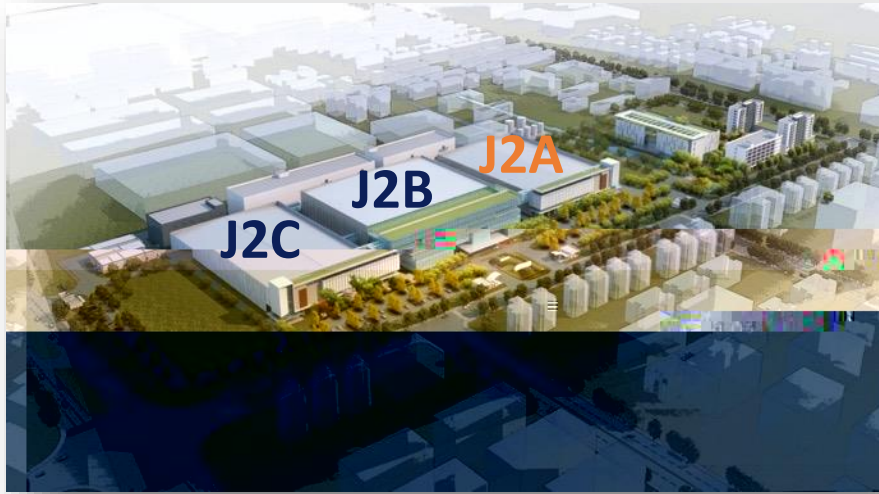
# World's First 5G mm-Wave Ultra-Wideband Dual Polarization Antenna Using Stacked Dielectric Resonator

*Cheng-Chuang Lin*

Senior Director, R&amp;D

SJ Semiconductor (Jiangyin), Corn

 SJSEMI CONNECT



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◆ s SO  
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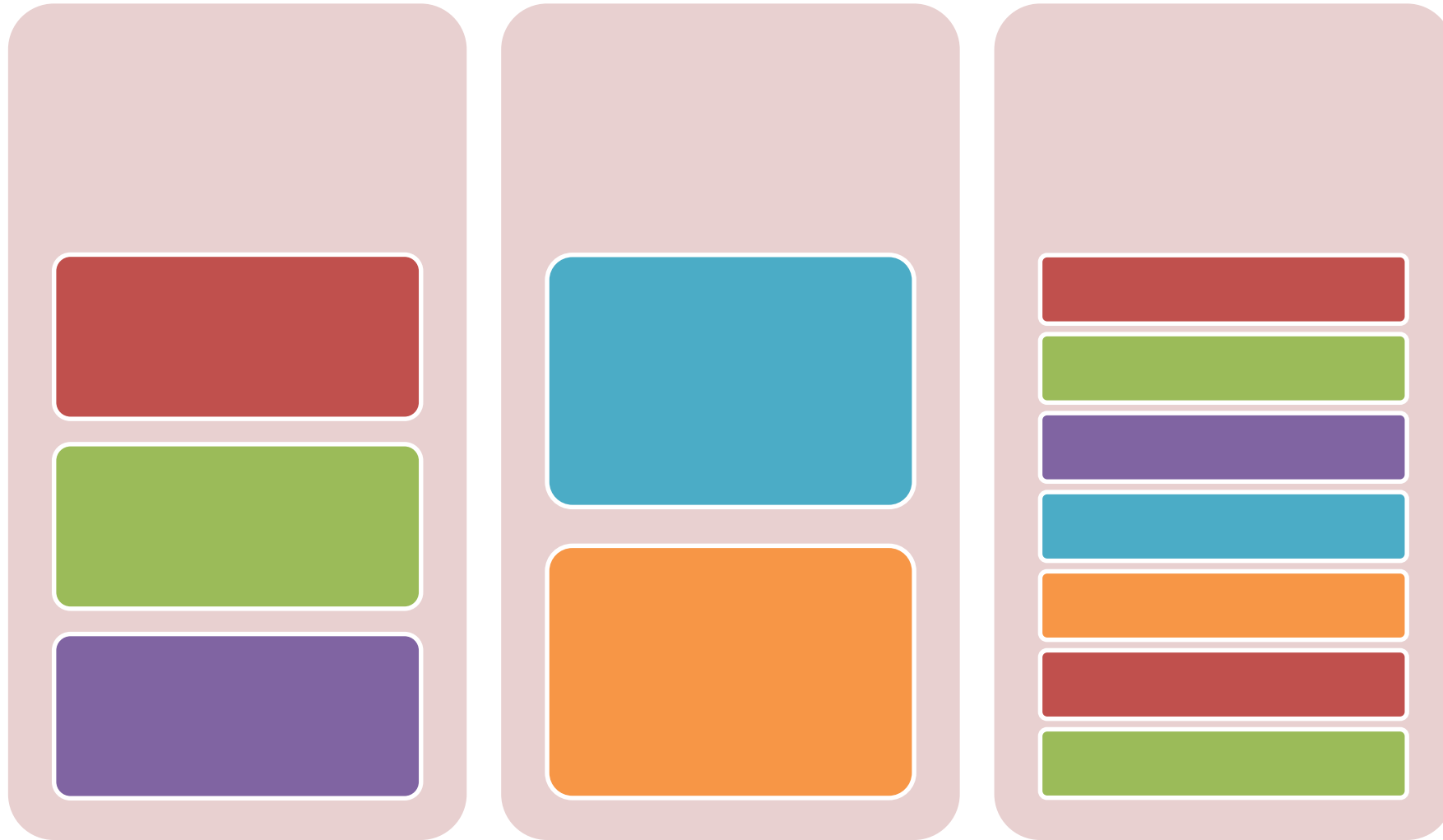






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